



17W

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:
Fernando Gonzalez et al.

Serial No.: 10/751,141

Filed: December 31, 2003

For: Transistor Having Vertical Junction
Edge and Method of Manufacturing
the Same

§
§
§
§
§
§
§
§
§

Group Art Unit: 2815

Examiner: Nguyen, Joseph H.

Atty. Docket: MICS:0114
03-0027

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313

CERTIFICATE OF TRANSMISSION OR MAILING 37 C.F.R. 1.8	
I hereby certify that this correspondence is being transmitted by facsimile to the United States Patent and Trademark Office in accordance with 37 C.F.R. 1.6(d) or is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313, on the date below:	
May 16, 2005	<i>Kerri Hyland</i>
Date	Kerri Hyland

Sir:

RESPONSE AND AMENDMENT

In response to the Official Action mailed on February 14, 2005, Applicants respectfully request reconsideration of the above-identified application in view of the remarks and amendments set forth below.